AMENDMENTS TO THE SPECIFICATION:

Please replace paragraph 0020 with the following:

[0020] The output of the filter controls a numerically controlled ~~counter~~ oscillator (NCO) 46, which is designed to produce an output signal (line 47) of selected frequency, i.e. 8 kHz in this particular embodiment. It could, however, be other integral multiples of 1 kHz, including 1 kHz, 2 kHz, 3 kHz, etc. The NCO in operation counts nominally to the output frequency, which in the embodiment shown is 8 kHz. The count is adjusted by the output of filter (+/-); the adjustment allows the system to lock to the incoming signal. The output of counter 46 is applied to the feedback circuit 50 for the phase-locked-loop. The feedback circuit 50 converts the 8 kHz signal to a 1 kHz signal, which is then applied to the phase detector 42 for comparison with the signal from the edge detector.